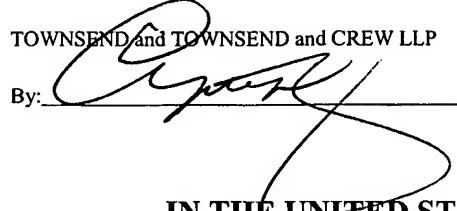


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On 3/22/06

TOWNSEND and TOWNSEND and CREW LLP

By: 

PATENT
Attorney Docket No.: 022193-042810US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Michael Peters

Application No.: 10/782,386

Filed: February 18, 2004

For: METHOD AND CIRCUIT FOR
INCREASING THE MEMORY ACCESS
SPEED OF AN ENHANCED
SYNCHRONOUS SDRAM

Customer No.: 20350

Confirmation No. 4341

Examiner: Woo H. Choi

Technology Center/Art Unit: 2189

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed November 3, 2005, please enter the following amendments and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.